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DIALOG(R)File 351:Derwent WPI

009141382 **Image available**
WPI Acc No: 1992-268820/199232
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XRPX Acc No: N92-205588

Flat-panel display fabrication - using pixel arrays which form light valves or switches fabricated with control electronics in single crystal thin-film material

Patent Assignee: KOPIN CORP (KOPI-N); DINGLE B (DING-I); FAN J C C (FANJ-I) ; JACOBSEN J (JACO-I); MCCLELLAND R (MCCL-I); ZAVRACKY P M (ZAVR-I)
Inventor: DINGLE B; FAN J C C; JACOBSEN J; MCCLELLAND R; SPITZER M; ZAVRACKY P M
Number of Countries: 017 Number of Patents: 014
Patent Family:

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Priority Applications (No Type Date): US 90636602 A 19901231; US 91801885 A
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Patent Details:

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WO 9212453	A1		86	G02F-001/136	
Designated States (National): JP					
Designated States (Regional): AT BE CH DE DK ES FR GB GR IT LU MC NL SE					
US 5206749	A		28	G02F-001/1343	
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Designated States (Regional): AT BE CH DE DK ES FR GB IT LI LU MC NL SE					
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Abstract (Basic): WO 9212453 A

Fabrication of the panel display involves forming a single crystal semiconductor material on a supporting upstrate. An array of transistors and an array of pixel electrodes are formed in or on the single crystal material to form a circuit panel. Each pixel is actuatable by one of the transistors.

A light transmitter material is positioned adjacent to the circuit panel such that an electric field or signal generated by each pixel alters a light transmitting property of the material.

ADVANTAGE - Produces high quality image.

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Title Terms: FLAT; PANEL; DISPLAY; FABRICATE; PIXEL; ARRAY; FORM; LIGHT; VALVE; SWITCH; FABRICATE; CONTROL; ELECTRONIC; SINGLE; CRYSTAL; THIN; FILM; MATERIAL

Derwent Class: P81; P85; U14

International Patent Class (Main): G02B-026/00; G02F-001/1335; G02F-001/1343; G02F-001/136; G02F-001/1368; H01L-021/00; H01L-021/20; H01L-021/70; H01L-027/01; H01L-033/00

International Patent Class (Additional): G02F-001/1333; G02F-001/1345; G09C-003/10; H01L-021/44; H01L-021/84; H01L-027/00; H01L-027/12; H01L-029/00; H01L-029/04; H01L-031/0392; H05B-033/12

File Segment: EPI; EngPI

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